

PRELIMINARY PROGRAM

2003 IEEE Symposium on Security and Privacy

May 11-14, 2003
The Claremont Resort
Oakland, California, USA

sponsored by
IEEE Computer Society Technical Committee on Security and Privacy
in cooperation with
The International Association for Cryptologic Research (IACR)

Sunday, May 11, 2003

4:00-7:00	Registration and Reception
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Monday, May 12, 2003

8:45-9:00	Opening Remarks
9:00-10:30	Session: Anonymity <i>Mixminion: Design of a Type III Anonymous Remailer Protocol</i> George Danezis (Cambridge Univ.), Roger Dingledine, Nick Mathewson (Free Haven Project) <i>Probabilistic Treatment of MIXes to Hamper Traffic Analysis</i> Dakshi Agrawal (IBM Watson), Dogan Kesdogan, Stefan Penz (Aachen Univ. Tech.) <i>Defending Anonymous Communication Against Passive Logging Attacks</i> Matt Wright, Micah Adler, Brian Neil Levine, Clay Shields (U. Mass.)
10:30-11:00	Break
11:00-12:00	Session: IDS <i>Active Mapping: Resisting NIDS Evasion Without Altering Traffic</i> Umesh Shankar (UC Berkeley), Vern Paxson (ICSI) <i>Anomaly Detection Using Call Stack Information</i> Henry Hanping Feng (U. Mass.), Oleg M. Kolesnikov, Prahlad Fogla, Wenke Lee (Georgia Tech.), Weibo Gong (U. Mass.)
12:00-1:30	Lunch
1:30-2:30	Invited talk
2:30-3:00	Break
3:00-4:00	Session: OS <i>Defending Against Denial-of-Service Attacks with Puzzle Auctions</i> XiaoFeng Wang, Mike Reiter (CMU) <i>Pi: A Path Identification Mechanism to Defend against DDoS Attacks</i> Abraham Yaar, Adrian Perrig, Dawn Song (CMU)
4:00-6:00	5-minute talks

Tuesday, May 13, 2003

9:00-10:30	<p>Session: Formal Methods</p> <p><i>A Unified Scheme for Resource Protection in Automated Trust Negotiation</i> Ting Yu, Marianne Winslett (U. Illinois, Urbana-Champaign)</p> <p><i>Beyond Proof-of-compliance: Safety and Availability Analysis in Trust Management</i> Ninghui Li (Stanford), William H. Winsborough (NAI Labs), John C. Mitchell (Stanford)</p> <p><i>Intransitive Non-Interference for Cryptographic Purposes</i> Michael Backes, Birgit Pfitzmann (IBM Zurich)</p>
10:30-11:00	Break
11:00-12:00	<p>Session: Hardware</p> <p><i>Specifying and Verifying Hardware for Tamper-Resistant Software</i> David Lie, John Mitchell (Stanford), Chandramohan Thekkath (Microsoft Research), Mark Horowitz (Stanford)</p> <p><i>Using Memory Errors to Attack a Virtual Machine</i> Sudhakar Govindavajhala, Andrew W. Appel, (Princeton)</p>
12:00-1:30	Lunch
1:30-2:30	Invited talk
2:30-3:00	Break
3:00-4:00	<p>Session: Hardware & Crypto</p> <p><i>Secret Handshakes from Pairing-Based Key Agreements</i> D. Balfanz, G. Durfee (PARC), N. Shankar (U. Maryland), D.K. Smetters, J. Staddon, H.C. Wong (PARC)</p> <p><i>Random Key Predistribution Schemes for Sensor Networks</i> Haowen Chan, Adrian Perrig, Dawn Song (CMU)</p>

Wednesday, May 14, 2003

9:00-10:30	<p>Session: Distributed Systems</p> <p><i>Hardening Functions for Large Scale Distributed Computations</i> Douglas Szajda, Barry Lawson, Jason Owen (U. Richmond)</p> <p><i>A Practical Revocation Scheme for Broadcast Encryption Using Smart Cards</i> Noam Kogan, Yuval Shavitt, Avishai Wool (Tel Aviv Univ.)</p> <p><i>Using Replication and Partitioning to Build Secure Distributed Systems</i> Lantian Zheng, Stephen Chong, Andrew C. Myers (Cornell), Steve Zdancewic (U. Pennsylvania)</p>
10:30-11:00	Break
11:00-12:00	<p><i>Vulnerabilities in Synchronous IPC Designs</i> Jonathan S. Shapiro (Johns Hopkins)</p> <p><i>Garbage Collector Memory Accounting in Language-Based Systems</i> David W. Price, Algis Rudys, Dan S. Wallach (Rice)</p>